

S/N: _____

Inventor: Varghese et al

Title: Electronic Device Housing and Systems and Methods For Forming Same

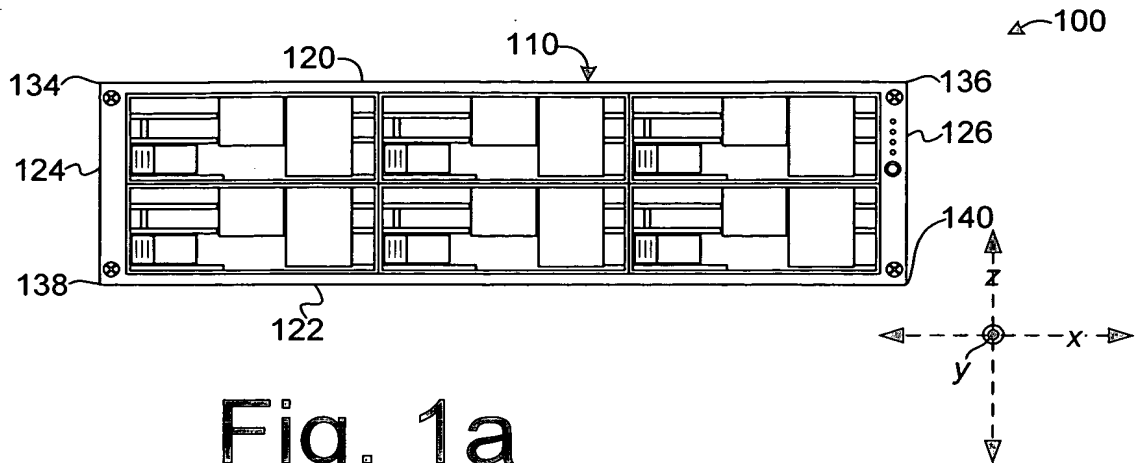


Fig. 1a
Prior art

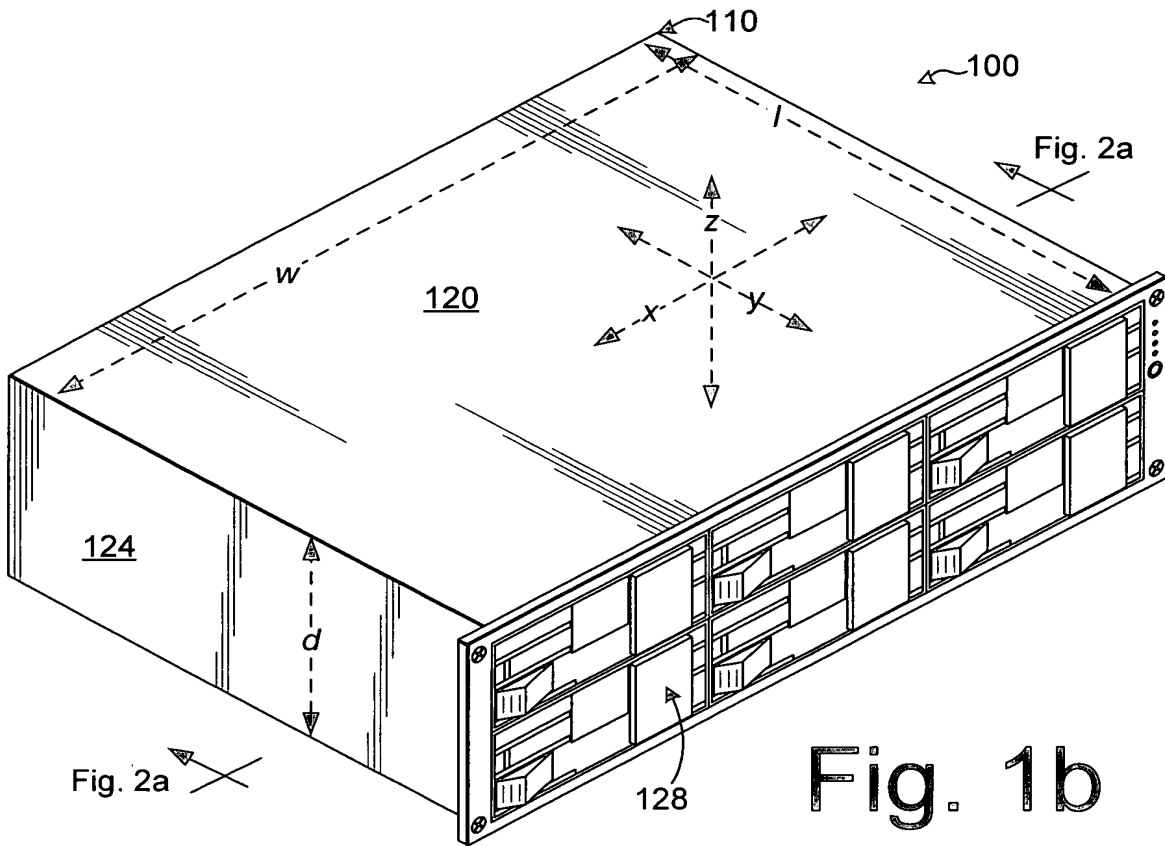
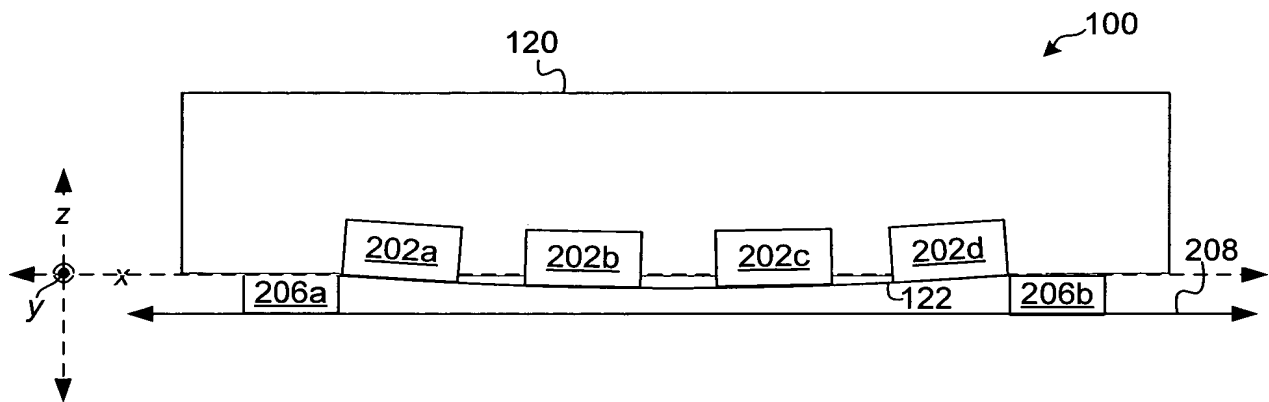
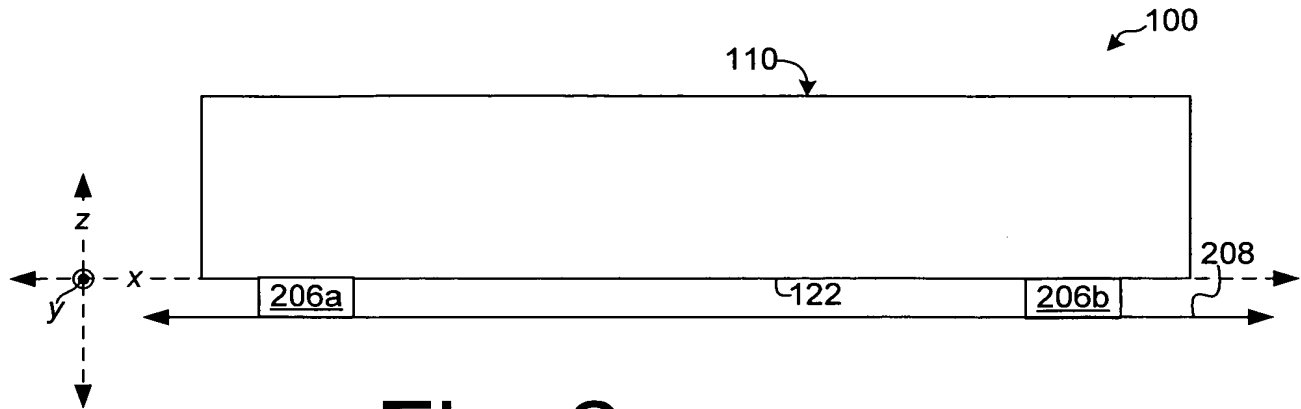


Fig. 1b
Prior art



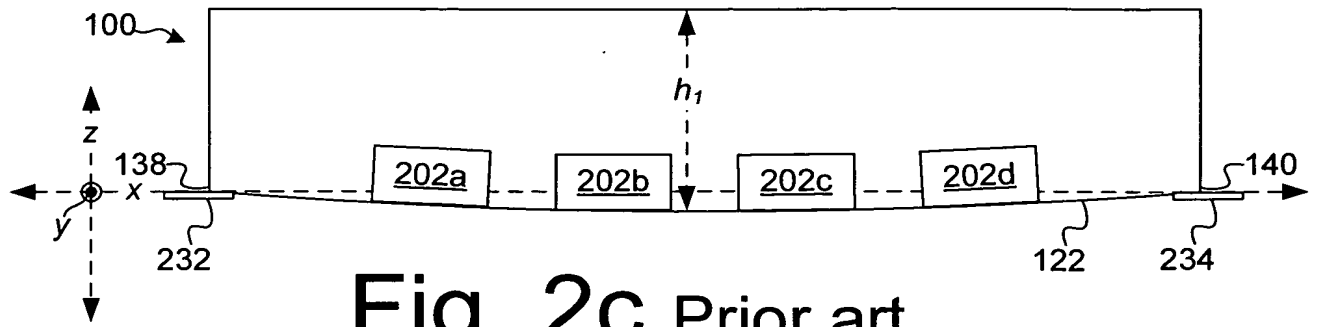


Fig. 2c Prior art

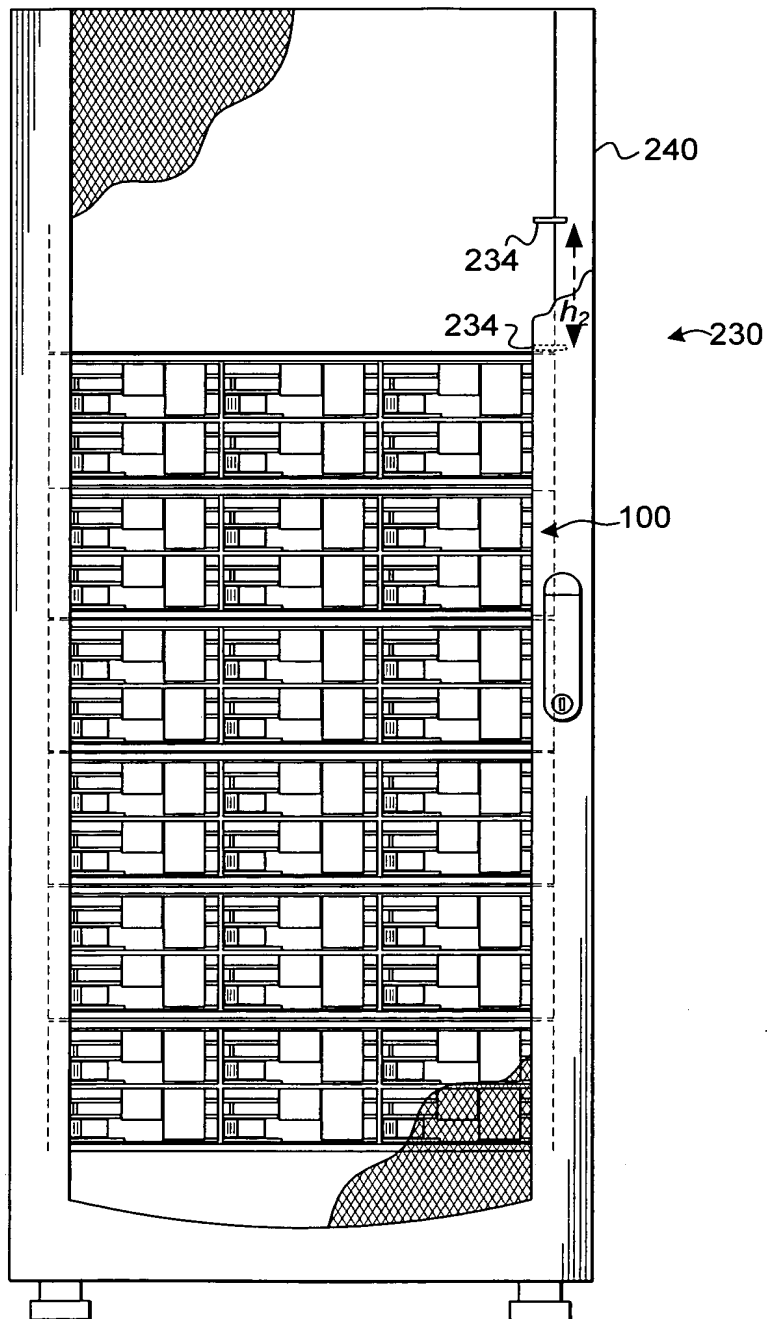
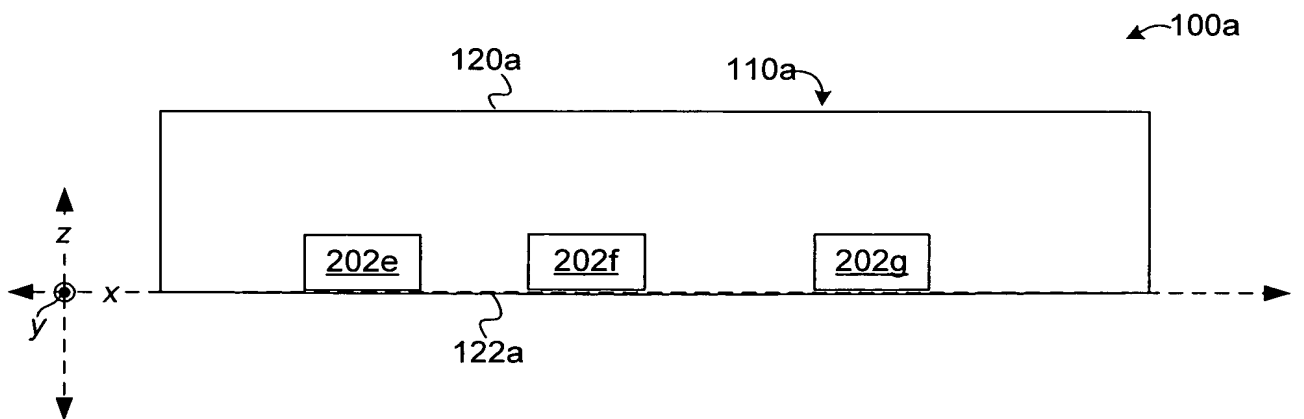
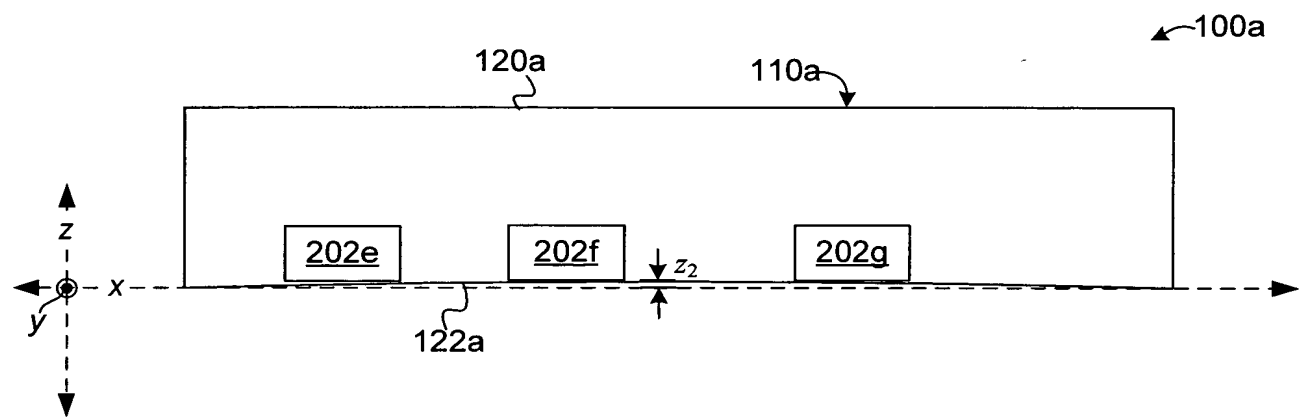
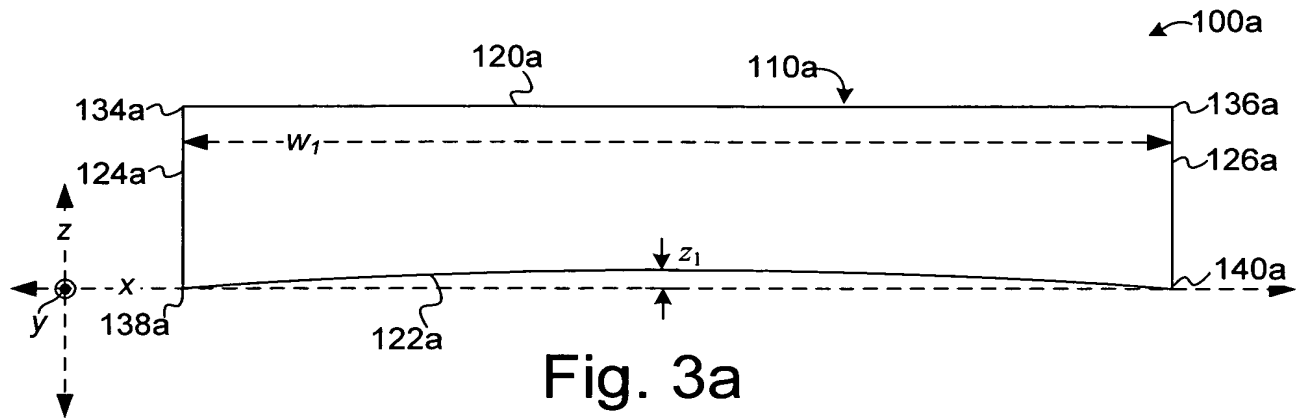


Fig. 2d
Prior art



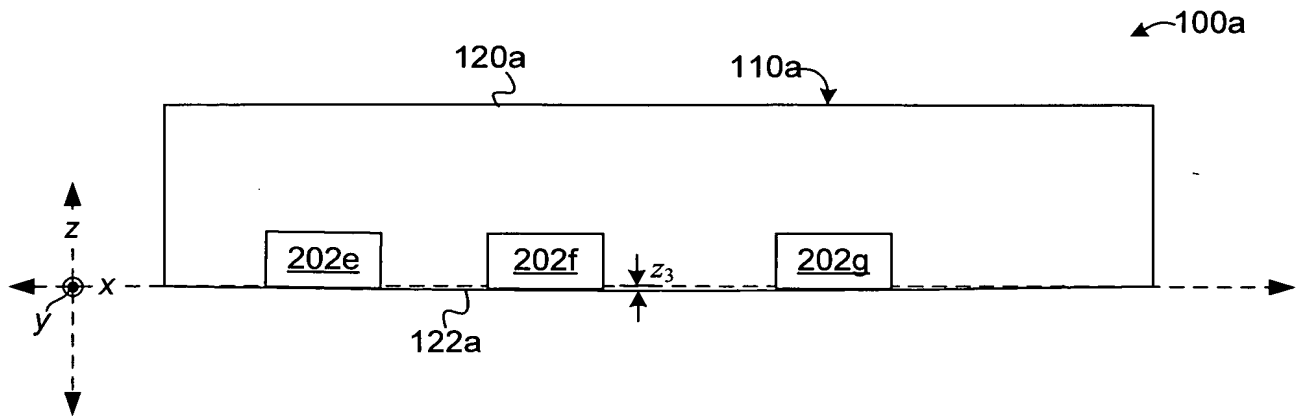
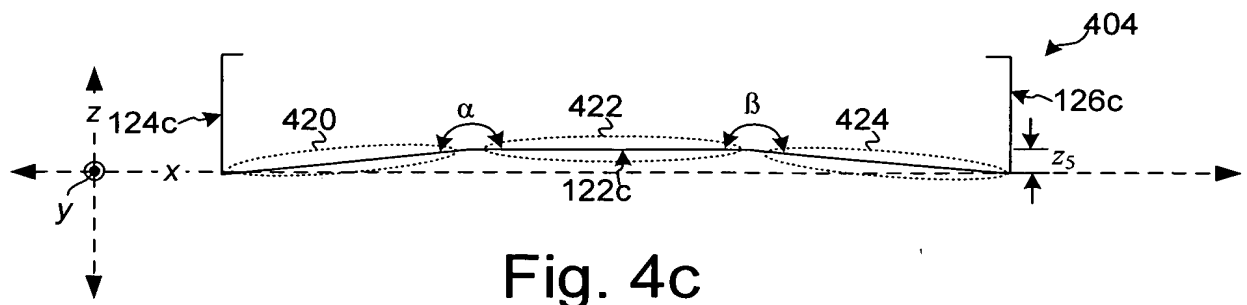
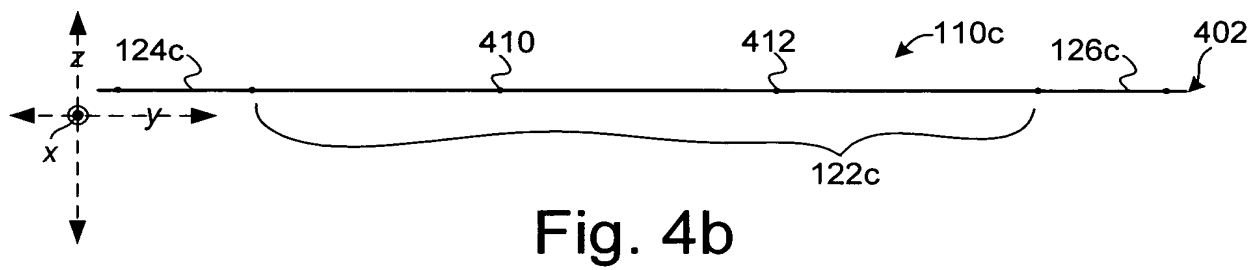
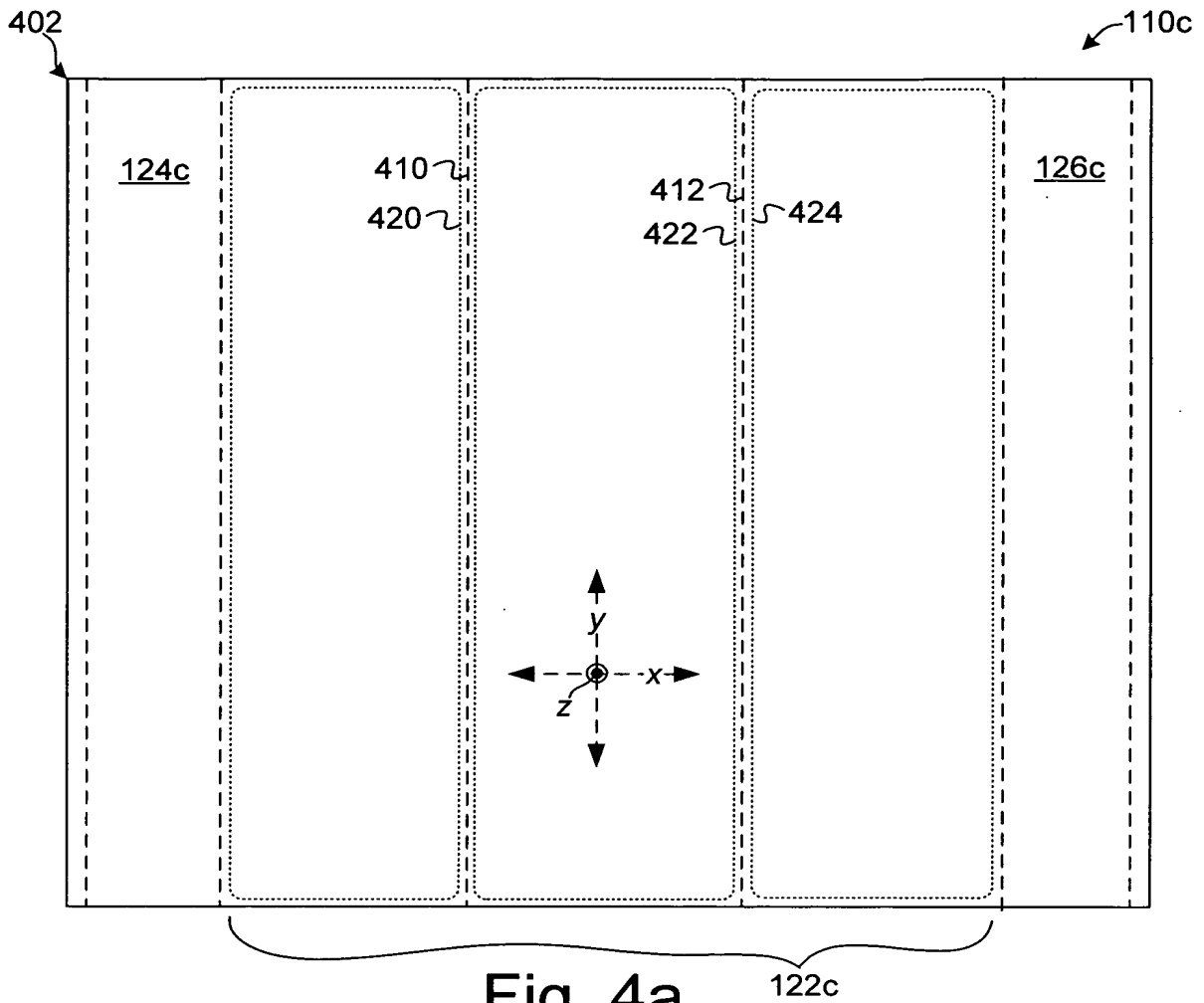
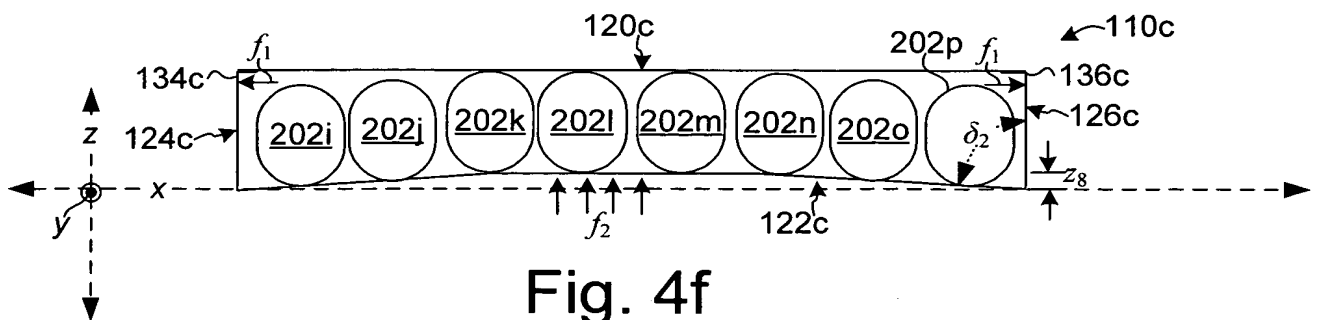
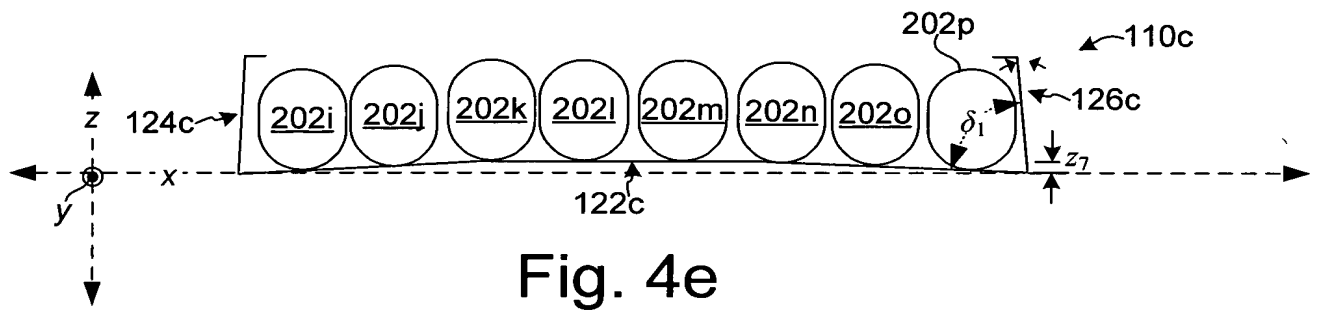
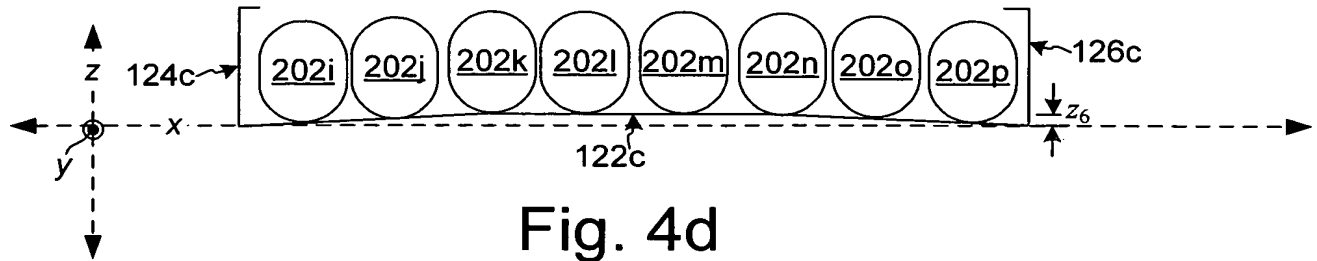


Fig. 3d





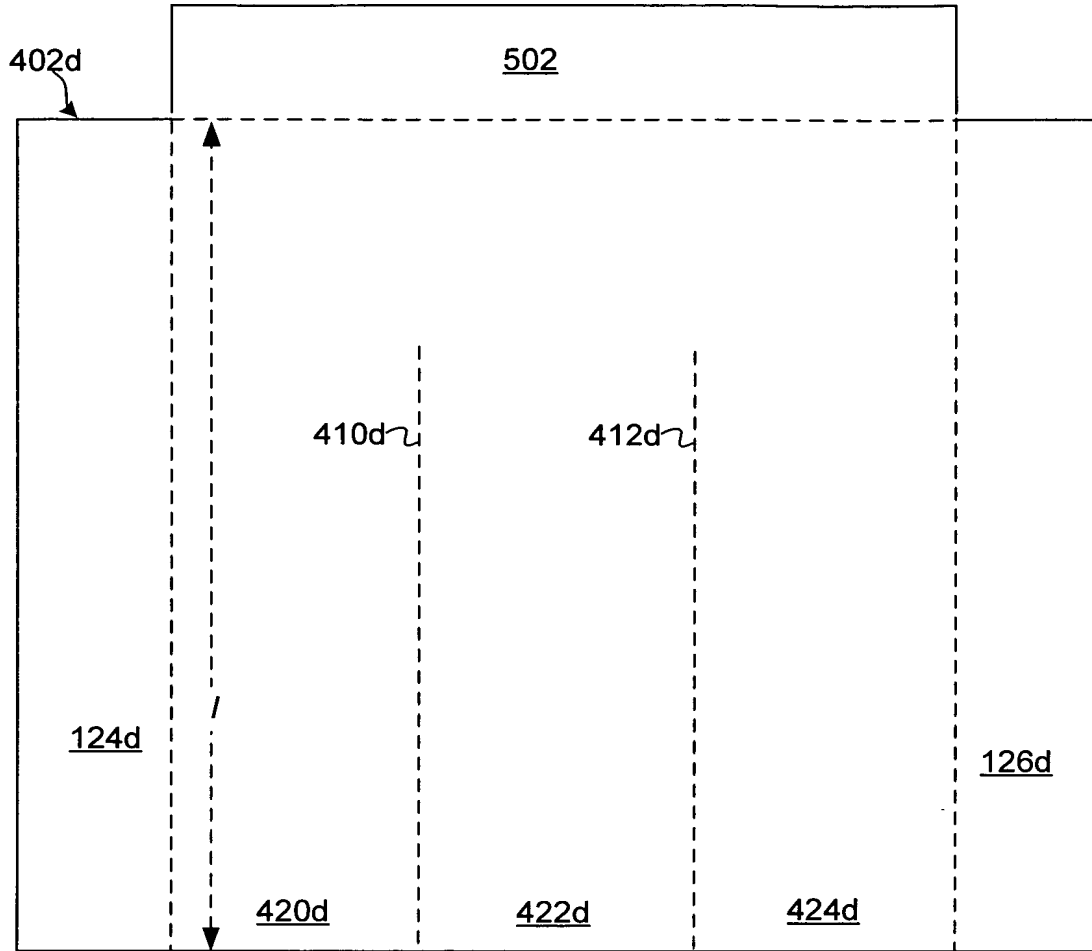


Fig. 5a

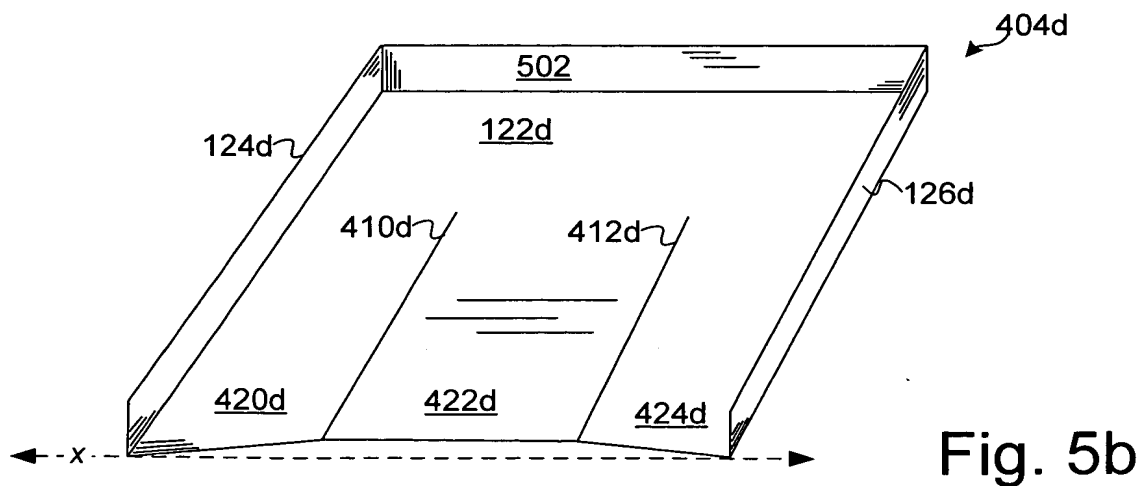


Fig. 5b

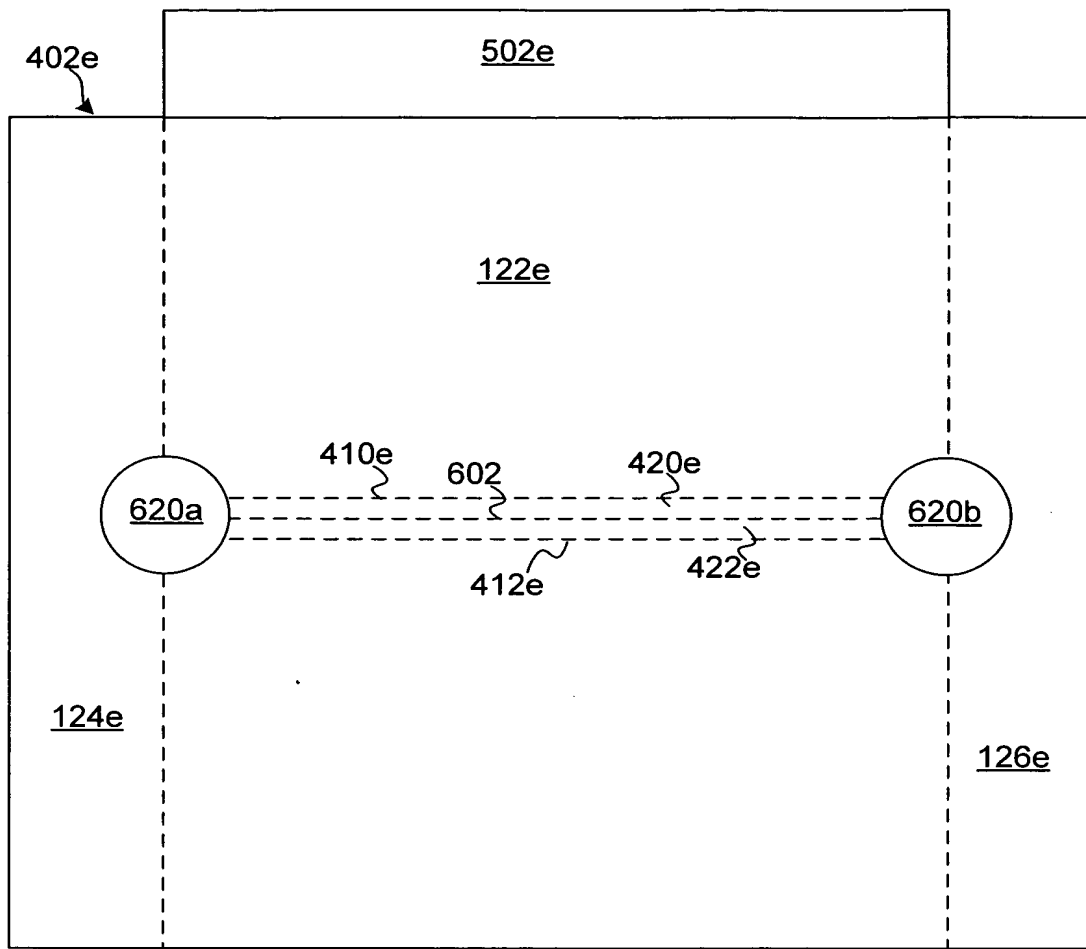


Fig. 6a

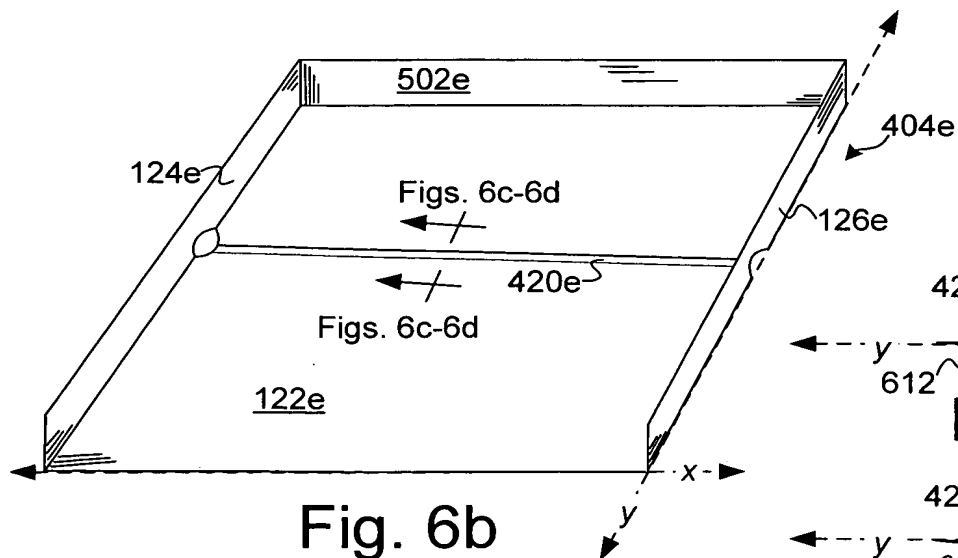


Fig. 6b



Fig. 6c

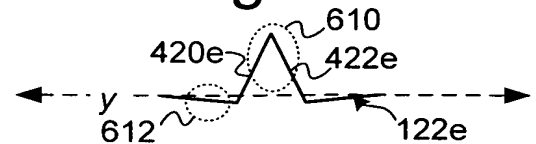


Fig. 6d